



A project of DIT, MCIT, Govt. of India at IIT Bombay and IISc

2nd INUP Hands-on Training Workshop on Nanofabrication Technologies July 06 – 10, 2010, IIT Bombay

<http://www.inup-iitb.org>

The Department of Information Technology (DIT), the Ministry of Communication and Information Technology (MCIT), Govt. of India has sponsored Centres of Excellence in Nanoelectronics (CEN) at IIT Bombay and IISc Bangalore. To make these facilities available for research and process/product development to researchers from other academic Institutions, R&D laboratories and industry, the Indian Nanoelectronics Users Programme (INUP) was also started. At present, INUP provides full financial support (fabrication/characterization cost, travel and stay) for the researchers from academia to execute their work at these Centres. For other external users, the fabrication/characterization would be carried out at subsidised rates. The full description of the INUP programme and available facilities can be seen on the website <http://www.inup-iitb.org>. The purpose of this workshop is to offer hands-on training to the prospective external users of CEN facilities at IITB.

The “Hands-on Training” takes a trainee through various steps of nanofabrication/characterization/simulation. The different training modules include; wafer cleaning, thermal oxidation, lithography and bulk micromachining etc.

Online applications are invited from interested researchers from academia, R&D laboratories, and industries for the participation in this workshop, in the prescribed registration form (link given below). While selecting participants, preference will be given to those faculty members/students who are implementing/have submitted R&D projects under INUP at IITB/ IISc Bangalore. Those who have attended any of the INUP Workshop on Nanofabrication Technologies (1st or 2nd or 3rd) held at either IITB/IISc and have not submitted a R&D project proposal to IITB or IISc will not be considered for attending the Hands-on Training Workshop. If you have a good and new idea, you are invited to submit a proposal to knages@inup-iitb.org in the prescribed form, which is available at <http://www.inup-iitb.org/forms.html>

If selected for participation in the workshop, all students and faculty members would be supported for travel by train, III tier AC and II AC respectively. Accommodation and food would be taken care of. However, expenditure towards local travel on both ends will be borne by you.

Important Dates:

Registration deadline : 30th May, 2010

Confirmation of Selection : 15th June, 2010

Contact

INUP Office, Microelectronics Group, Department of Electrical Engineering, Indian Institute of Technology, Bombay, Powai, Mumbai 400076 India

Phone: +91-22-2576 4472, Fax:+91-22-2572-3480 email:knages@inup-iitb.org Website: <http://www.inup-iitb.org> or <http://www.inup.org.in>